Interface Engineering via Sputtered Oxygenated CdS:O Window Layer for Highly Efficient Sb$_2$Se$_3$ Thin-Film Solar Cells with Efficiency Above 7%

Liping Guo, Baiyu Zhang, Smriti Ranjit, Jacob Wall, Swapnil Saurav, Adam J. Hauser, Guozhong Xing, Lin Li, Xiaofeng Qian,* and Feng Yan*

Antimony chalcogenide Sb$_2$Se$_3$ is an emerging photovoltaic absorber due to its appropriate bandgap ($\approx 1.1$ eV), high absorption coefficient ($>10^5$ cm$^{-1}$), suitable p-type conductivity, low toxicity, earth abundance, and excellent stability. However, the stringent growth condition and low photovoltage limit its power conversion efficiency (PCE). Herein, via a combined theoretical and experimental study, interface engineering via an oxygenated cadmium sulfide (CdS) window layer (CdS:O) is found to be an effective approach to improve the device performance of CdS:O/Sb$_2$Se$_3$ solar cells. The sputtered oxygenated CdS:O window layer can be used to replace conventional chemical-bath-deposited CdS window layer in the Sb$_2$Se$_3$ devices. The best PCE of 7.01% is demonstrated in the superstrate configuration of fluorine-doped SnO$_2$/CdS:O/Sb$_2$Se$_3$/graphite with a high open-circuit voltage of 0.432 V, where Sb$_2$Se$_3$ is fabricated using the close space sublimation technique. The interfacial diffusion between Sb$_2$Se$_3$ and sputtered CdS:O is significantly suppressed by introducing oxygen at the interface, which prevents Cd diffusion and the formation of Cd interstitials. Combined device physics characterizations and theoretical calculations reveal that oxygen in the CdS:O/Sb$_2$Se$_3$ interface can increase depletion region, built-in voltage, and reduce interfacial recombination. These findings provide the guidance to optimize quasi-one-dimensional non-cubic earth-abundant chalcogenide photovoltaic devices through interface engineering.

1. Introduction

Efficient, stable, and earth-abundant thin-film photovoltaic materials are highly desired for harvesting solar energy and providing renewable and affordable energy. Recently, low-cost non-cubic Sb$_2$Se$_3$ has become a promising photovoltaic absorber candidate with orthorhombic structure (space group Pbnm). It possesses unique quasi-one-dimensional ribbons, i.e., (Sb$_4$Se$_9$)$_n$, featuring anisotropic and effective photoexcited carrier transport. The desired optical bandgap ($\approx 1.1$ eV) and superior absorption coefficient ($>10^5$ cm$^{-1}$) endow Sb$_2$Se$_3$ with great potential for applications in thin-film solar cells. The power conversion efficiency (PCE) of superstrated FTO/CdS/Sb$_2$Se$_3$ thin-film solar cells was boosted close to $\approx 7.6\%$, and superstrated Mo/MoSe$_2$/Sb$_2$Se$_3$/CdS solar cells have achieved 9.2% PCE. Several techniques have been used to grow high-quality Sb$_2$Se$_3$ absorber layer, including close space sublimation (CSS), vapor transport deposition (VTD), and rapid thermal evaporation (RTE). The quasi-one-dimensional (Sb$_4$Se$_9$)$_n$ ribbon orientation is significantly impacted by the film deposition parameters, such as substrate temperature, source temperature, and the post-sensitization of the deposited film. Remarkably, a wide range of the window layer materials (e.g., ZnO, TiO$_2$, and CdS) have been used to tune the ribbon orientation and suspend the device degradation. However, the champion device performance is still far from its theoretical PCE limit ($\approx 31\%$). The photovoltaic device performance is severely limited by the low open-circuit

L. Guo, J. Wall, S. Saurav, Dr. S. Li, Dr. F. Yan
Department of Metallurgical and Materials Engineering
The University of Alabama
Tuscaloosa, AL 35487, USA
E-mail: fyan@eng.ua.edu

B. Zhang, Dr. X. Qian
Department of Materials Science and Engineering
Texas A&M University
College Station, TX 77843, USA
E-mail: feng@tamu.edu

S. Ranjit, Dr. A. J. Hauser
Department of Physics and Astronomy
The University of Alabama
Tuscaloosa, AL 35487, USA

Dr. G. Xing
United Microelect. Corp. Ltd.
Singapore 519528, Singapore

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voltage (V_{OC}) of ≈0.4 V in contrast to its bandgap of 1.1 eV due to abundant deep defects,[13] although the short-circuit current density (J_{SC}) was improved to ≈30 mA cm^{-2}.[14] Given the well-known Cu(In/Ga)Se (CIGS) (≈22% PCE) technology with similar bandgap (≈1.1 eV) and its V_{OC} record of ≈0.744 V, it is worth developing effective approaches to lift the V_{OC} in Sb_{2}Se_{3} solar cells.[15]

Sputtered oxygenated CdS (CdS:O) window layer can improve the V_{OC} in CdTe and other chalcogenide solar cells (such as Cu_{2}BaSnS_{4}) due to its high resistivity and transparency.[18–21] The CdS:O window layer is composed by CdS and CdSO_{4} with a wide bandgap range (2.4–3.7 eV), which can be achieved by engineering oxygen composition during the reactive sputtering deposition.[20] Moreover, the high resistivity of CdS:O window layers can passivate the absorber/CdS:O interface and suspend the interface recombination.[20] Thus, both V_{OC} and J_{SC} can be improved by introducing sputtered oxygenated CdS:O to CdTe devices.[22] To date, the most widely used window layer for high-efficiency Sb_{2}Se_{3} cells is the chemical bath deposited (CBD) CdS window layer.[16,7] Therefore, it is necessary to conduct a systematic investigation on the effect of sputtered oxygenated CdS:O window layer on the Sb_{2}Se_{3} devices. Meanwhile, the presence of oxygen in CdS window layers can optimize the interdiffusion at the CdS/CdTe interface.[23] The existence of oxygen in CdTe solar cells can increase the acceptor concentration and lifetime.[24] Thus, the oxygen in CdS and CdTe can improve the CdS/CdTe device performance. It has also been reported that introducing oxygen during the Sb_{2}Se_{3} deposition could passivate interface defects and lead to enhanced device performance.[13] However, the impact of oxygen in the sputtered oxygenated CdS:O window layer on the Sb_{2}Se_{3} device performance is unclear.

Here, we present our first-principles theoretical calculations and experimental studies to understand the role of interfacial oxygen in the CdS:O/Sb_{2}Se_{3} heterostructure. Our first-principles calculations indicate the interdiffusion mechanism and defect chemistry associated with the oxygenated CdS:O window layer–based Sb_{2}Se_{3} solar cells. Oxygen has a higher diffusion barrier across (Sb_{2}Se_{3})_{n} ribbons (i.e., perpendicular to the ribbon direction) than that along with the ribbons inside van der Waals (vdW) gap. Controlling oxygen content in the interfacial area of CdS:O/Sb_{2}Se_{3} can effectively passivate the vDW gap of Sb_{2}Se_{3}, thereby blocking the Cd diffusion into Sb_{2}Se_{3} to form n-type Cd interstitials. Our theoretical study suggests that higher V_{OC} can be achieved in the Sb_{2}Se_{3} thin-film solar cells with interface engineering. Following the theoretical prediction, we propose to replace the conventional CBD CdS layer by sputtered oxygenated CdS:O window layer to passivate the interface between the window layer and Sb_{2}Se_{3}. By systematic tuning of oxygen concentration in the sputtered oxygenated CdS:O layer, we achieved the highest V_{OC} (≈0.432 V), J_{SC} (≈29.9 mA cm^{-2}), and PCE (≈7.01%) in the CSS-grown Sb_{2}Se_{3} devices with the superstrate F:SnO_{2}/CdS:O/Sb_{2}Se_{3}/graphite/Ag device architecture. The reactively sputtered oxygenated CdS:O window layer effectively enhances the bandgap of CdS:O and tailors the (Sb_{2}Se_{3})_{n} ribbons to (211)-textured orientation. The grain size of Sb_{2}Se_{3} grown on the sputtered CdS:O is larger than that on CBD CdS, suggesting that the oxygen in the CdS:O can also impact the Sb_{2}Se_{3} growth. The interfacial element distribution analysis indicates that the interfacial oxygen can suppress the CdS/Sb_{2}Se_{3} interdiffusion, which prevents the Cd interstitial formation in Sb_{2}Se_{3} absorber layer. In addition, the device physics analysis demonstrated that the increased V_{OC} stems from the improved built-in voltage, and the improved J_{SC} comes from the thicker depletion region and the reduced interfacial recombination in the CdS:O/Sb_{2}Se_{3} device. This study of extrinsic point defects based on first-principles density functional theory (DFT) further proves that oxygen-associated defects, substitutional defects (O_{Se} and O_{Sb}) and interstitial defect (O_{i}), hardly become n-type. However, Cd interstitial (Cd_{i}) is a donor defect; therefore to reduce its concentration, the Se-rich condition is desired. These theoretical and experimental findings demonstrate that interface engineering is an effective approach to improve the device performance of Sb_{2}Se_{3} thin-film solar cells, which can be used to break the photovoltage limit in Sb_{2}Se_{3} solar cells.

2. Results and Discussion

To elucidate the role of oxygen in the interface between CdS and Sb_{2}Se_{3}, we first investigate the diffusion behavior using first-principles DFT calculations. Two diffusion pathways were considered in the calculations for both Cd and O atoms (Figure 1). One diffusion pathway is along with the (Sb_{2}Se_{3})_{n} ribbons as shown in Figure 1a,b for Cd and O, respectively. The other diffusion pathway is across the (Sb_{2}Se_{3})_{n} ribbons, as shown in Figure 1d,e. The calculated diffusion energy barriers are shown in Figure 1c,f for the diffusion across and along with ribbons, respectively. As shown in Table 1, the diffusion barrier of Cd along ribbons is ≈0.6 eV, whereas Cd diffusion across ribbons is about 0.8 eV. The diffusion barrier of O across ribbons is ≈1.7 eV, which is much higher than ≈1 eV for O diffusion along with ribbons. In both cases, Cd has a lower diffusion barrier than that of O, suggesting that Cd diffusion is relatively faster than O. It may be related to their degraded stabilities due to diffusion.[13] The high diffusion energy barriers of oxygen suggest that oxygenating the CdS/Sb_{2}Se_{3} interface may fill the vDW gaps and block Cd diffusion.

In experiments, Cd and S could be unintentionally introduced from the buffer layer CdS to the absorber layer Sb_{2}Se_{3}. We, therefore, study the formation energies of three potential impurity elements in Sb_{2}Se_{3}, namely O, Cd, and S, associated point defects, including substitution and interstitial defect types. Although Sb_{2}Se_{3} is a binary semiconductor, it is structurally multinary due to its low symmetry. The unit cell of antimony selenide has a space group of Pbnm, consisting of two quasi-one-dimensional Sb_{2}Se_{3} chains, i.e., 8 Sb atoms and 12 Se atoms. Among the total 20 atoms per unit cell, there are two Sb sites and three Se sites which are symmetrically inequivalent (labeled in Figure 1e), resulting in the structurally multinary nature with complicated defect properties as recently pointed out by Huang et al.[13] Thus, two extreme conditions were considered together with multiple inequivalent Sb and Se sites: 1) Se-rich condition and 2) Se-poor condition, as shown in Figure 2a,b, respectively. Since the intrinsic defects in the Sb_{2}Se_{3} absorbers have been investigated,[15,23] we focus on nine types of extrinsic defects, including three interstitial defects Cd_{i}, S_{i}, and O_{i} and six
substitutional defects Cd\(_{\text{Se}}\), Cd\(_{\text{Sb}}\), S\(_{\text{Sb}}\), O\(_{\text{Se}}\), and O\(_{\text{Sb}}\). As mentioned earlier, among the substitutional defects, there are three different Se sites and two different Sb sites, resulting in \(X_{\text{Se1}}\), \(X_{\text{Se2}}\), \(X_{\text{Se3}}\), \(X_{\text{Sb1}}\), and \(X_{\text{Sb2}}\), where \(X\) is Cd, S, or O. In general, the defect formation energy diagrams (Figure 2a,b) and the transition level diagram (Figure 2c) show that the substitutional defects at the symmetrically inequivalent sites in Sb\(_2\)Se\(_3\) lead to dispersed transition levels and defect formation energies, especially in the case of \(S_{\text{Se1}}\) and \(S_{\text{Se2}}\). This is similar to the case of intrinsic point defects reported by Huang et al.\(^{[15]}\) Furthermore, under the Se-rich condition (Figure 2a), O\(_{\text{Se}}\), S\(_{\text{Se}}\), O\(_{\text{Sb}}\), and Cd\(_{\text{Sb}}\) have relatively low formation energies below 1.0 eV. For the Cd-related defects (e.g., Cd\(_{\text{Se}}\), Cd\(_{\text{Sb}}\), and Cd\(_{\text{Se}}\)), the donor defects Cd\(_{\text{Se}}\) and Cd\(_{\text{Sb}}\) have relatively high defect formation energy compared with that of the Cd\(_{\text{Se}}\) substitutional defect (Figure 2a), whereas the Cd\(_{\text{Sb}}\) defect is a shallow acceptor defect with transition level of \(\epsilon(1/0) = 0.1\) eV above the valence band maximum (VBM) (Figure 2c). It also shows that Cd\(_{\text{Sb}}\) could improve the p-type conduction of the Sb\(_2\)Se\(_3\). However, it was experimentally reported that the Cd atoms diffusing into the Sb\(_2\)Se\(_3\) and forming Cd\(_i\) could produce a buried homojunction and lead to n-type conduction in Sb\(_2\)Se\(_3\).\(^{[26]}\) Indeed, our results reveal that, when Cd stays in the vdW gap between (Sb\(_4\)Se\(_6\))\(_n\) ribbons as Cd\(_i\) defect, it becomes a shallow donor with transition level \(\epsilon(2/0)\) located at \(\approx 0.7\) eV (relatively close to conduction band minimum [CBM], Figure 2c), thereby causing n-type conduction. For the S-induced point defects (i.e., S\(_i\), S\(_{\text{Sb}}\), and S\(_{\text{Se}}\)), both S\(_i\) and S\(_{\text{Sb}}\) have formation energy above 1 eV, but the S\(_{\text{Se}}\) substitutional defect has almost zero defect formation energy, suggesting that S could randomly replace Se in the Sb\(_2\)Se\(_3\). This low S\(_{\text{Se}}\) substitutional defect formation energy can also be explained by the complete solid solution of ternary Sb\(_2\)(S, Se).\(^{[27]}\) Finally, among three O-related point defects (i.e., O\(_i\), O\(_{\text{Se}}\), and O\(_{\text{Sb}}\)), O\(_{\text{Se}}\) has the lowest formation energy, indicating that O tends to replace Se. O\(_i\) interstitial defect has relatively low formation energy, implying that it is likely that oxygen will be located at the vdW gaps between the (Sb\(_4\)Se\(_6\))\(_n\) ribbons. Under the Se-poor condition, the S\(_{\text{Se}}\) defects have the lowest defect formation energy, and the Cd\(_i\) defects are also easy to form due to its low formation energy, whereas the other defects associated with CdS:O window

Table 1. Diffusion energy barrier for Cd and O across and along with the (Sb\(_4\)Se\(_6\))\(_n\) ribbons.

<table>
<thead>
<tr>
<th>Diffusion element</th>
<th>Across ribbons [eV]</th>
<th>Along with ribbons [eV]</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cd</td>
<td>0.8</td>
<td>0.6</td>
</tr>
<tr>
<td>O</td>
<td>1.7</td>
<td>1.0</td>
</tr>
</tbody>
</table>

Figure 1. Calculated Cd and O diffusion pathways and energy barriers in Sb\(_2\)Se\(_3\): a) Cd and b) O diffusion along with ribbons; c) diffusion energy barrier along with ribbons; d) Cd and e) O diffusion across ribbons; and f) diffusion energy barrier across ribbons. Symmetrically inequivalent Sb and Se sites are labeled in e) including Sb1, Sb2, Se1, Se2, and Se3.
layers have higher formation energy (above 1 eV). This observation agrees with the reported data that Sb$_2$Se$_3$ under the Se-poor condition has poor conductivity and low device performance in general. To improve the device performance, it is necessary to perform post-selenization for the deposited films to suppress the donor defects like Cd$_i$ at the Se-poor condition.$^{[25]}$
The above first-principles electronic structure calculations indicate that introducing oxygen at the CdS/Sb₂Se₃ interface can form stable O due to relatively low defect formation energy, high O diffusion barrier, and filled the vdW gaps between Sb₂Se₃ ribbons with benign transition levels. The latter can effectively reduce Cd induced n-type conduction. More specifically, the oxygenated sputtered CdS:O window layer could also provide oxygen to occupy the Se sites (O_{Se}) in the Sb₂Se₃, where O_{Se} has the lowest formation energy without generating detrimental charge defect (Figure 2a). Partial oxygen can also enter into the vdW gaps between the (Sb₂Se₆)ₙ ribbons, which will block and suppress the Cd diffusion, thereby preventing the formation of Cdₜ defect in the bulk of Sb₂Se₃. In fact, the oxygen diffusion barrier across/along ribbons is high (∼1.7/1.0 eV), as shown in Figure 1. Therefore, the oxygen will accumulate at the CdS:O/Sb₂Se₃ interface. Considering that Cdₜ exhibits a shallow n-type transition level, which is ∼0.2 eV from CBM (Figure 2c), the suppressed Cdₜ formation will reduce the n-type buried heterostructure. Thus, we can expect the enhanced device performance in oxygenated CdS:O/Sb₂Se₃ devices by tuning the interfacial oxygen composition. The schematic energy band diagram is shown in Figure 2d.

In light of the above theoretical insights, we deposit Sb₂Se₃ absorbers on sputtered oxygenated CdS:O films at slightly Se-rich condition during the CSS process and investigate the impact of oxygen content in the Sb₂Se₃ solar absorber material. The sputtered oxygenated CdS:O window layers were characterized using both the X-ray diffraction (XRD) and X-ray photoelectron spectroscopy (XPS). The sputtered pure CdS without oxygen shows a hexagonal crystalline structure, whereas the oxygenated CdS:O layers show nanocrystalline structure with reduced grain size (Figure S1c,d, Supporting Information). Meanwhile, the XPS measurements on the sputtered oxygenated CdS layers indicate that the CdSO₃ and CdSO₄ components are formed in the sputtered oxygenated CdS:O (Figure S2, Supporting Information). Figure 3a shows the UV–vis absorption spectra of the sputtered oxygenated CdS:O films. The inset shows the optical image of CdS:O films with O₂/(O₂ + Ar) of 0%, 1%, 2%, and 3%. The transmittance increases with increasing oxygen partial pressure. The as-deposited CdS, i.e., O₂/(O₂ + Ar) ∼0%, shows a strong absorption at the blue range (<500 nm), which is similar to the CBD CdS (Figure S1, Supporting Information). The bandgap, indicated by the dashed line in Figure 3b, increases from 2.4 to 2.85 eV as more oxygen was introduced in the

![Figure 3](https://www.solar-rrl.com)

**Figure 3.** a) UV–vis absorbance spectra for the oxygenated CdS, and the inset shows the oxygenated CdS:O samples with increasing oxygen partial pressure of 0%, 1%, 2%, and 3% O₂ during deposition; b) absorption coefficient extracted from the optical transmittance; c) XRD pattern of the CSS-deposited Sb₂Se₃ films; and d) TC determined from the Sb₂Se₃ films grown on oxygenated CdS:O window layer.
The increased bandgap was attributed to the formation of the Cd sulfuric oxide (CdSO₄) and sulfur compounds (CdSO₃) with wide bandgaps.[20] Figure 3c shows the XRD pattern of CdS:O/Sb²Se₃ films deposited at identical CSS conditions by controlling the oxygenated CdS:O window layers only. The Sb²Se₃ films show orthorhombic structure (space group Pbnm) in agreement with the PDF-0015-0861. To inspect the texture effect by the CdS:O window layer, the texture coefficients (TCs) were calculated, as shown in Figure 3d. It was observed that the TCs gradually change to (211)-orientation, whereas the (120)-orientation was reduced when more oxygen was introduced into CdS:O. The increased TCs of (211)-textured orientation with increasing oxygen in the oxygenated sputtered CdS:O can be associated with the decreased CdS:O grain size and crystallinity, as shown in the atomic force microscopy (AFM) topography (Figure S1c, Supporting Information). It is known that (211)-textured orientation can benefit the device performance with lower series resistivity.[21] Figure S2a,b, Supporting Information, shows the optical absorption and extracted bandgap for a representative CdS:O/Sb²Se₃ film grown at 2% oxygen partial pressure. The direct and indirect bandgaps are 1.21 and 1.16 eV, respectively, consistent with the previous study.[8]

Figure 4 shows the statistic distribution of Sb²Se₃ device parameters measured under air mass (AM) 1.5G. It demonstrates that V_OC, J_SC, R_SH, and FF improve with the increased oxygen in CdS:O until 2% O₂. The corresponding PCEs are also significantly elevated, and the series resistance R_S was decreased accordingly. The improved V_OC mainly originates from the reduced carrier recombination at the CdS:O/Sb²Se₃ heterostructure interface, i.e., the recombination of the holes from Sb²Se₃ and the electrons from CdS:O layers due to the band misalignment and lattice mismatch between CdS:O and Sb²Se₃. The formation of highly resistive CdSOₓ (x = 3 or 4) can passivate the CdS:O/Sb²Se₃ interface to prevent the carrier recombination. The highest V_OC of ≈0.432 V can be achieved by depositing CdS:O at 2% oxygen partial pressure. However, as the oxygen partial pressure further increases to ≈3%, the series resistance R_S increases significantly (Figure 4d), suggesting that more CdS was converted into insulating CdSO₄ with increasing the oxygen content, which prevents the electron transport in the window layer. By increasing oxygen partial pressure from 0% to 2%, the mean J_SC was dramatically increased from ≈25.5 to 29 mA cm⁻² (Figure 4c), indicating more photons were absorbed in the short-wavelength exciting electron-hole pairs. The improved J_SC is associated with the enhanced transparency of CdS:O, which was also confirmed by external quantum efficiency (EQE) measurement shown in Figure 5b. However, with excess oxygen (>2%) introduced into the CdS:O layer, the J_SC decreases due to the spike-like (type-I) conduction band alignment between CdS:O and Sb²Se₃ (Figure 2d). In other words, the CBM of CdS:O (3% O) is higher than that of Sb²Se₃. This CBM offset will reduce electron transport from Sb²Se₃ to the front FTO even if more light is absorbed in the absorber layer. Here, the sharply increased FFs are attributed to the reduced series resistivity, R_S, as shown in Figure 4e. The obtained optimal sputtering oxygen partial pressure for CdS:O is ≈2%, which agrees with the observation of CdS:O used for CdTe devices in various groups.[21,28,29]

Figure 5a shows the J–V curves of champion Sb²Se₃ cells with the CdS:O window layers sputtered with varying oxygen partial pressure at ambient temperature. The corresponding cell parameters are listed in Table 2. The best CdS:O/Sb²Se₃ device was obtained using the window layer sputtered at 2% oxygen partial pressure, where a PCE of 7.01%, a V_OC of 0.432 V, an FF of 54%, and a J_SC of ≈29.87 mA cm⁻² were achieved with CdS:O bandgap of ≈2.6 eV. Figure 5b shows that the EQE curves of these CdS:O/Sb²Se₃ devices, where the blue-shift of EQE at short wavelength is originated from the increased bandgap of CdS:O/Sb²Se₃, OC 29.87 mA cm⁻², © 2019 WILEY-VCH Verlag GmbH & Co. KGaA, Weinheim.

Figure 4. Statistic distribution of Sb²Se₃ device performance parameters with the FTO/CdS:O/Sb²Se₃/graphite/Ag architecture. The sputtered oxygenated CdS:O window layers were grown in the O₂/(Ar + O₂) ratio at 0%, 1%, 2%, and 3% at ambient temperature. a) Open-circuit voltage (V_OC), b) PCE, c) short-circuit photocurrent (J_SC), d) series resistance (R_S), e) fill factor (FF), and f) shunt resistance (R_SH).
sputtered oxygenated CdS:O window layers, whereas the EQE response at long wavelength (>900 nm) remains unchanged.

In addition to the gain at the short wavelength from the increased bandgap of CdS:O, the EQE response at shorter wavelength (e.g., from 500 to 900 nm) is also improved to 90% at 570 nm when oxygen partial pressure is increased from 0% to 2%, which can be attributed to the low recombination loss of photoexcited carriers (or long carrier diffusion length) in bulk Sb$_2$Se$_3$ with CdS:O sputtered at 2% oxygen partial pressure.

Table 2. Device performance parameters of FTO/CdS:O/Sb$_2$Se$_3$/graphite/Ag solar cells with sputtered oxygenated CdS:O window layers grown in the O$_2$/Ar+O$_2$ ratio at 0%, 1%, 2%, and 3% at ambient temperature.

<table>
<thead>
<tr>
<th>CdS:O</th>
<th>$V_{OC}$ [V]</th>
<th>$J_{SC}$ [mA cm$^{-2}$]</th>
<th>FF [%]</th>
<th>PCE [%]</th>
<th>$R_s$ [Ω cm$^2$]</th>
<th>$R_{sh}$ [Ω cm$^2$]</th>
</tr>
</thead>
<tbody>
<tr>
<td>0%</td>
<td>0.402 ± 0.05</td>
<td>26.75 ± 0.5</td>
<td>33.27 ± 0.5</td>
<td>3.59 ± 0.2</td>
<td>163.58 ± 1</td>
<td>1077.55 ± 5</td>
</tr>
<tr>
<td>1%</td>
<td>0.419 ± 0.05</td>
<td>26.71 ± 0.5</td>
<td>40.34 ± 0.5</td>
<td>4.52 ± 0.2</td>
<td>146.86 ± 1</td>
<td>1535.97 ± 5</td>
</tr>
<tr>
<td>2%</td>
<td>0.432 ± 0.05</td>
<td>29.87 ± 0.5</td>
<td>54.00 ± 0.5</td>
<td>7.01 ± 0.2</td>
<td>71.26 ± 1</td>
<td>1916.95 ± 5</td>
</tr>
<tr>
<td>3%</td>
<td>0.424 ± 0.05</td>
<td>28.24 ± 0.5</td>
<td>46.32 ± 0.5</td>
<td>5.56 ± 0.2</td>
<td>117.12 ± 1</td>
<td>1423.92 ± 5</td>
</tr>
</tbody>
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To ascertain the difference between the sputtered oxygenated CdS:O window layers and the conventional CBD deposited CdS window layer. We compare our sputtered CdS:O/Sb$_2$Se$_3$ champion devices with the best champion CBD CdS based Sb$_2$Se$_3$ devices deposited using the identical CSS conditions in our group. The sputtered CdS:O and CBD CdS films thickness is ≈60 nm. Figure 6a, b and Table 3 show the J–V and EQE, and device parameters of the champion Sb$_2$Se$_3$ device using sputtered and CBD CdS window layer, respectively. Overall, the
The local oxygen distribution at the interfacial area of these devices is investigated by secondary ion mass spectroscopy (SIMS) measurement. Figure 7g,h show the SIMS spectra of Sb, Se, Cd, S, and O in CBD CdS/Sb₂Se₃ and sputtered CdS:O/Sb₂Se₃ devices, respectively. Deeper Cd diffusion of ≈200 nm in CBD CdS/Sb₂Se₃ is observed, compared with that of ≈130 nm in the sputtered CdS:O/Sb₂Se₃ devices (Figure S4, Supporting Information). We may rule out the substrate temperature influence because it is identical during Sb₂Se₃ CSS deposition. However, the oxygen level is higher, and the Cd level is lower in Sb₂Se₃ with the sputtered CdS:O window layer than those with the CBD CdS window layer, confirming our theoretical results and suggesting that the oxygen from the CdS:O layer can suppress the Cd diffusion into Sb₂Se₃ at the junctions and prevent n-type buried homojunction located deep inside the Sb₂Se₃ absorber due to Cd interstitials.[28] Note that the oxygen in the sputtered oxygenated CdS:O window layer is in the SOₓ bonding environments, whereas the oxygen in the CBD CdS may be at trace level which is not incorporated with the Cd and S ions.[90] Thus, higher oxygen-concentration gradient at the interface of sputtered oxygenated CdS:O/Sb₂Se₃ will drive a deeper oxygen diffusion in the Sb₂Se₃ and block the Cd diffusion.

To better understand the mechanism of the improved device performance through the oxygenated interface in the CdS:O/Sb₂Se₃ devices compared with that of the CBD CdS/Sb₂Se₃ devices, we conducted the admittance spectroscopy, capacitance–voltage (C–V), and doping profiling characterization. Figure 8a shows the frequency dependence of the capacitance. The capacitance of the CdS:O/Sb₂Se₃ device is less frequency-dependent than that of CBD CdS/Sb₂Se₃, suggesting a reduced trap density in CdS:O/Sb₂Se₃ device. The Mott–Schottky analysis as shown in Figure 8c is performed by analyzing the C–V profile under dark at room temperature based on

\[
\frac{1}{C^2} = \frac{2q}{\varepsilon \varepsilon_0 A N_{bi}} (V_{bi} - V),
\]

where \(C\) is capacitance, \(q\) is the electronic charge, \(A\) is device area, \(\varepsilon_0\) is the permittivity of vacuum, \(\varepsilon\) is the relative dielectric constant of Sb₂Se₃, \(N\) is carrier concentration, and \(V_{bi}\) is the built-in potential. The \(V_{bi}\) of the Sb₂Se₃ solar cells can be determined through the \(1/C^2\)-Voltage curve as shown in Figure 8b. The voltage intercept is 0.52 and 0.41 V for sputtered oxygenated CdS:O and CBD CdS buffered Sb₂Se₃, respectively, indicating increased built-in electric field in CdS:O/Sb₂Se₃ devices. Thus, the increased \(V_{bi}\) can reduce photogenerated charge accumulation at the interfacial area and lead to improved \(V_{OC}\) in CdS:O/Sb₂Se₃ devices. Figure 8c shows the C–V doping profiles for both sputtered CdS:O/Sb₂Se₃ and CBD CdS/Sb₂Se₃ devices, which provide insights about the interfacial area defects. The C–V doping density in CBD CdS/Sb₂Se₃ devices is \(\approx 2.35 \times 10^{16} \text{cm}^{-3}\), and the corresponding depletion width is \(\approx 207\) nm. Importantly, for the sputtered CdS:O/Sb₂Se₃, the doping density slightly decreases to \(2.08 \times 10^{16} \text{cm}^{-3}\), and the depletion width is extended to 265 nm. A broader depletion zone (i.e., a decrease of capacitance) could collect more photogenerated carrier and contribute to larger photocurrent (\(J_{ph}\)). The reduced carrier doping density in the sputtered CdS:O/Sb₂Se₃ may originate from the depletion region mainly located at the CdS:O/Sb₂Se₃ interface. The interfacial defect density is estimated to be \(4.86 \times 10^{12} \text{cm}^{-2}\) and \(5.51 \times 10^{12} \text{cm}^{-2}\) for CBD CdS/Sb₂Se₃ and sputtered CdS:O/Sb₂Se₃ devices, respectively, suggesting that the

<table>
<thead>
<tr>
<th>Device</th>
<th>(V_{OC}) [V]</th>
<th>(J_{SC}) [mA cm(^{-2})]</th>
<th>FF [%]</th>
<th>PCE [%]</th>
<th>(R_s) [(\Omega \text{ cm}^2)]</th>
<th>(R_{sh}) [(\Omega \text{ cm}^2)]</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sputter CdS:O</td>
<td>0.432</td>
<td>29.87</td>
<td>54.00</td>
<td>7.01</td>
<td>71.26</td>
<td>1916.95</td>
</tr>
<tr>
<td>CBD CdS</td>
<td>0.414</td>
<td>29.11</td>
<td>50.80</td>
<td>6.12</td>
<td>64.02</td>
<td>1671.81</td>
</tr>
</tbody>
</table>

Sb₂Se₃ cells with sputtered CdS:O window layer outperform the Sb₂Se₃ devices with CBD CdS window layer, particularly in terms of the \(V_{OC}\) and FF. These device performance comparisons suggest a better diode behavior in the sputtered CdS:O/Sb₂Se₃ devices. The higher shunt resistance of sputtered CdS:O/Sb₂Se₃ devices also contributes to the improved FF, although the series resistance is slightly higher than that of the Sb₂Se₃ device with CBD CdS window layer. The EQE spectra were also measured under bias voltage conditions (−0.5 V) and the ratio of EQE responses in the form of \(\frac{\text{EQE} (−0.5 \text{V})}{\text{EQE} (0 \text{V})}\), as shown in Figure 6c. An increase toward the longer wavelength was observed for the CBD CdS/Sb₂Se₃ devices, suggesting more collection of photocurrents at the rear side of Sb₂Se₃ with an internal electric field. However, the EQE ratio is approximately flat over the long wavelength (i.e., >600 nm) in the sputtered CdS:O/Sb₂Se₃ devices, indicating more photogenerated carriers without the assistance of internal electric field. Interestingly, for the short wavelength (<600 nm), the sputtered CdS:O window layer can collect more photogenerated carriers with the existence of an internal electric field than that of the CBD-deposited CdS, indicating that sputtered oxygenated CdS:O window layers are highly efficient for the carrier collection. Similar behavior was also observed with a solar cell capacitance simulator (SCAPS) device simulation, as shown in Figure S5, Supporting Information.

To characterize the microstructure origin of the improved device performance in CdS:O/Sb₂Se₃ solar cells, scanning electron microscopy (SEM) is used to analyze the surface morphology and crystallization of Sb₂Se₃ films. Figure 7a,b shows the top-view images of Sb₂Se₃ with CBD CdS and sputtered CdS:O (2%), respectively. The cross-sectional SEM image and energy dispersive spectroscopy (EDS) elemental mapping are shown in Figure 7c for the Sb₂Se₃ grown on sputtered CdS:O film (2%), suggesting that film thickness around 900 nm and the Sb/Se elements stabilize in the film. The grain size of the Sb₂Se₃ film grown on the sputtered CdS:O window layer is larger than that of the Sb₂Se₃ with the CBD CdS window layer, although the CSS growth conditions are identical. The grain-size distributions of the two SEM morphology images are shown in Figure 7d. The average grain size of Sb₂Se₃ with CBD CdS window layer is about 290 nm, whereas that of Sb₂Se₃ grown on sputtered CdS:O window layer is about 480 nm. The larger grain size of Sb₂Se₃ on the sputtered CdS:O window layer suggests that the grain-growth process is kinetically faster than that of Sb₂Se₃ on the CBD CdS. Moreover, the grain orientation prefers (211)-orientation in CdS:O/Sb₂Se₃, whereas the grain prefers (221)-orientation in CBD CdS/Sb₂Se₃ film (Figure S1, Supporting Information). The surface states of the Sb₂Se₃ grown on the sputtered CdS:O layer were characterized using the XPS. The Sb 3d and Se 3d spectra in Figure 7e,f indicate no clear trace of Sb₂O₃ and residual Se.
The sputtered CdS:O/Sb$_2$Se$_3$ interface has more active defects contributing to the improved device performance. Figure 8d shows the Nyquist plots characterized by electrochemical impedance spectroscopy (EIS) to elucidate the interfacial charge transport kinetics in the devices. The internal series resistance ($R_s$) is 29.19 and 21.28 Ω and the recombination resistance ($R_{rec}$) is 821.4 and 1059 Ω for CdS:O/Sb$_2$Se$_3$ and sputtered CdS:O/Sb$_2$Se$_3$ devices, respectively, indicating that the carrier accumulation and recombination are weak in the sputtered CdS:O/Sb$_2$Se$_3$ devices. Thus, in terms of the interfacial charge transport behavior, we may conclude that the sputtered CdS:O window layer could provide a wider-depletion region in Sb$_2$Se$_3$ absorber layer to collect more photogenerated carrier. Moreover, the sputtered CdS:O/Sb$_2$Se$_3$ device also exhibits a higher built-in voltage and lower recombination interface, which contribute to the increased $V_{OC}$ and $J_{SC}$.

3. Conclusions

In summary, through a combined theoretical and experimental study, we have demonstrated an effective interface engineering...
approach to increase the Sb$_2$Se$_3$ photovoltaic device performance by using sputtered oxygenated CdS (CdS:O) as a window layer by replacing the conventional CBD-deposited CdS window layer. The open-circuit voltage, photocurrent, and fill factor are all improved with the best PCE of \( \approx 7.01\% \) (\( V_{OC} \) of 0.432 V, FF of 54\%, and \( J_{SC} \) of \( \approx 29.87 \text{ mA cm}^{-2} \)) in the CdS:O/Sb$_2$Se$_3$ device. The benefit of the sputtered oxygenated CdS:O window layer for the Sb$_2$Se$_3$ devices originates from the improved bandgap of CdS:O with high resistivity of CdSO$_x$, and transmittance that allows more light absorption in the Sb$_2$Se$_3$ absorber. Meanwhile, both theoretical calculations and experimental results show that the sputtered oxygenated CdS:O window layer can prevent the formation of Cd interstitial defects in the Sb$_2$Se$_3$ layer due to the accumulation of the oxygen at the interface between CdS:O and Sb$_2$Se$_3$ layers. Our work suggests that the optimization of the window layer can greatly improve the open-circuit voltage and enhance the device performance of the emerging antimony chalcogenide Sb$_2$Se$_3$ and Sb$_2$S$_3$ thin-film solar cells.

4. Experimental Section

Films Deposition and Characterization: The sputtered pure CdS and oxygenated CdS (CdS:O) window layers were deposited by radio frequency (RF) sputtering system (AJA ATC Orion 5) of a CdS target (2-in. in diameter) at ambient temperature. The base pressure for the sputtering was pumped down to 7 \( \times 10^{-8} \text{ Torr} \). The oxygenated CdS:O layers were reactively sputtered in a mixture gas (\( \approx 5 \text{ mTorr} \)) between O$_2$ and Ar gas (99.999\% purity, Airgas, USA) at a total flow rate of 20 sccm. The oxygen partial pressure (O$_2$/Ar + O$_2$) was controlled using the O$_2$ and Ar mass flow to 0\%, 1\%, 2\%, and 3\%, respectively. Oxygenated CdS:O films were deposited on F: SnO$_2$-coated soda lime glass (FTO, NSG, USA) with a thickness around 80 nm via controlling the deposition time and power. As a comparison, the CBD CdS was also fabricated on the FTO glass. The Sb$_2$Se$_3$ films with thickness \( \approx 1 \mu\text{m} \) were grown by CSS, as reported by Guo et al.\textsuperscript{[8]} The Sb$_2$Se$_3$ film deposition used a source and substrate temperature at 550 and 300 °C, respectively, and the deposition pressure was about 10 mTorr. The optical properties for the CdS and Sb$_2$Se$_3$ films were characterized using the UV–vis spectrometer (Shimadzu UV-1800). The crystalline structure of CdS:O and Sb$_2$Se$_3$ film were characterized by XRD with Cu Kα radiation (Philips X’Pert). Surface morphology of CdS:O was investigated by atomic force microscopy (AFM; Park XE70) and a scanning...
The total energy calculations were based on the energy dispersive spectroscopy (EDS) attached to the SEM. XPS was collected using the Kratos Axis 165 XPS with Al source.

**Solar Cell Fabrication and Characterization:** The Sb$_2$Se$_3$ thin-film solar cells with the sputtered oxygenated CdS:O windows layer were fabricated with conventional superstrate device structure: glass/FTO/CdS:O/Sb$_2$Se$_3$/Graphite/Ag. Graphite back contact was screen printed onto films followed by Ag deposition by sputtering. The cells area was $\approx 0.08 \text{ cm}^2$. The current-density–voltage ($I$–$V$) curves of the CdS:O/Sb$_2$Se$_3$ devices were measured by the Newport Sol3A class AAA solar simulator (Oriel, model 94023A, Newport Corporation, Irvine, CA, USA) under ambient conditions (room temperature, 1000 W m$^{-2}$, AM 1.5 G illumination). A Keithley 2420 SourceMeter (Keithly Instrument Inc.) was used to acquire $I$–$V$ characteristics. EIS measurements were carried out using Solartron Analytical 1260 impedance analyzer equipped with a 1296 dielectric interface at a frequency of 10 kHz, and the AC amplitude was 50 mV. DC bias was changed from $-0.3$ to 0.3 V. EIS measurements were carried out using Solartron Analytical 1260 impedance analyzer at a bias potential of 0.5 V in dark with the frequency range of $10^{-2}$–$10^{4}$ Hz.

**First-Principles Electronic Structure Calculations:** Cd and O diffusion energy barriers in Sb$_2$Se$_3$ were calculated using the climbing image nudged elastic band (NEB) method. The total energy calculations were based on first-principles DFT as implemented in the Vienna Ab initio Simulation Package (VASP). The Perdew-Burke-Ernzerhof (PBE) form of exchange-correlation functional within the generalized gradient approximation (GGA) and a plane wave basis set with a 400 eV energy cutoff were used in the DFT calculations. The structural optimization of bulk Sb$_2$Se$_3$ was performed with a Γ-centered Monkhorst-Pack k-point sampling grid of $4 \times 4 \times 12$, a maximal residual force of less than 0.01 eV Å$^{-1}$ and an energy convergence criterion of $10^{-6}$ eV for electronic relaxation. The optB86 vdW nonlocal correlation functional was adopted to account for the weak vdW interactions between ribbons. The calculated lattice parameters $a$, $b$, and $c$ of the unit cell are 11.482, 11.870, and 4.014 Å, respectively. A supercell of $1 \times 1 \times 3$ was constructed to determine the diffusion energy barrier in the dilute limit.

**First-Principles Calculations of Point Defects in Sb$_2$Se$_3$:** For defect formation energy and transition levels, we used the finite-size correction scheme proposed by Fresyoldt et al. and Oba and co-workers. The unit cell structure was enlarged to $2 \times 2 \times 6$ supercell, containing 480 atoms, to minimize the errors caused by image charge interaction. The charged defect formation energy is defined by $\Delta E_i = E(D, q) - E(\text{bulk}) - \sum n_i \varepsilon_i + q_i E_{\text{corr}}$. Here $E(D, q)$ is the total energy of the defect supercell with charge $q$, and $E(\text{bulk})$ is the total energy of pristine cell without any defect, $n_i$ involved in the defect, and $q_i$ corresponds to the number of atoms added ($n_i < 0$) or removed ($n_i > 0$). $E_{\text{corr}}$ is the Fermi level using the VBM as the reference. $E_{\text{corr}}$ refers to the correlation term. It mainly consists of two parts: the electrostatic potential alignment between the defect cell and the bulk, and the interaction between the charge and its images due to the periodic boundary condition used in the DFT calculations.

**Supporting Information**

Supporting Information is available from the Wiley Online Library or from the author.

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**Conflict of Interest**

The authors declare no conflict of interest.

**Keywords**

first-principles calculations, interdiffusion, interface engineering, Sb$_2$Se$_3$ thin-film solar cells, sputtered oxygenated CdS